IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

NOV 1 7 2003

In re application of:

Kiichi HIRANO, et al.

Serial No.:

09/813,866

Filed:

March 22, 2001

For:

SEMICONDUCTOR DEVICE DISPLAY AND METHOD OF

FABRICATING THE SAME

PETITION FOR EXTENSION OF TIME

Commissioner of Patents P.O. Box 1450 Alexandria, VA 22313-1450

November 17, 2003

Confirmation No.: 6213

Examiner: S. Gebremariam

Docket No.: 107318-0000

Art Unit: 2811

Sir:

Applicants petition the Commissioner of Patents and Trademarks to extend the time for response to the Office Action dated June 16, 2003 for two (2) months from September 16, 2003 to November 16, 2003 (November 16, 2003 falls on a Sunday).

A check in the amount of One Thousand One Hundred and Eighty Dollars (\$770 to cover the cost of the filing and \$410.00 to cover the cost of the extension fee) is enclosed. In the event any fees are required, please charge our Deposit Account No. 01-2300.

11/18/2003 MGEBREM1 00000054 012300 09813866

02 FC:1252

10.00 DA

410.00 OP

Respectfully submitted,

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